



28th International Conference on  
**Field Programmable Logic and Applications**

27 - 31 August, 2018 | Trinity College Dublin, Ireland | [fpl2018.org](http://fpl2018.org)

## CALL FOR PAPERS

**Abstract Submission (mandatory):** ~~March 19~~ **March 26, 2018**  
**Full Paper Submission Deadline:** ~~April 2~~ **April 9, 2018**  
**Submission page:** <https://easychair.org/conferences/?conf=fpl2018>

The International Conference on **Field-Programmable Logic and Applications (FPL)** is the first and remains the largest conference covering the rapidly growing area of field-programmable logic and reconfigurable computing. The conference continues to bring together researchers and practitioners from both academia and industry and from around the world. During the past 27 years, many of the advances in reconfigurable system architectures, applications, embedded processors, design automation methods and tools were first published in the proceedings of the FPL conference series.

The 28th edition will take place at Trinity College Dublin, Ireland, from August 27<sup>th</sup> until August 31<sup>st</sup>, 2018. The main conference will take place Monday through Wednesday while Tutorials and Workshops will run Thursday and Friday.

Authors are invited to submit original and unpublished contributions as **6 page papers in IEEE double column** format to be considered as regular papers (up to 2 additional pages can be purchased for accepted full papers). Submissions accepted as **posters will have 4 pages** and extended **abstracts for PhD forum contributions appear with 2 pages** in the proceedings. The conference proceedings will be published at **IEEE Xplore**. Authors of selected papers will be invited to submit extended versions to a special issue in **ACM TRETS**. All contributions must be submitted electronically in PDF format. The PhD forum is intended as a venue for PhD students to present their work in progress and preliminary results in a special poster session to receive feedback from other researchers.

Contributions within (but not limited to) the following five conference tracks are welcome:

- Architectures and Technology
- Applications and Benchmarks
- Design Methods and Tools
- Accelerated Machine Learning
- Surveys, Trends, and Education

Michela Blott, Suhaib Fahmy and Miriam Leeser, **Technical Programme Co-Chairs**